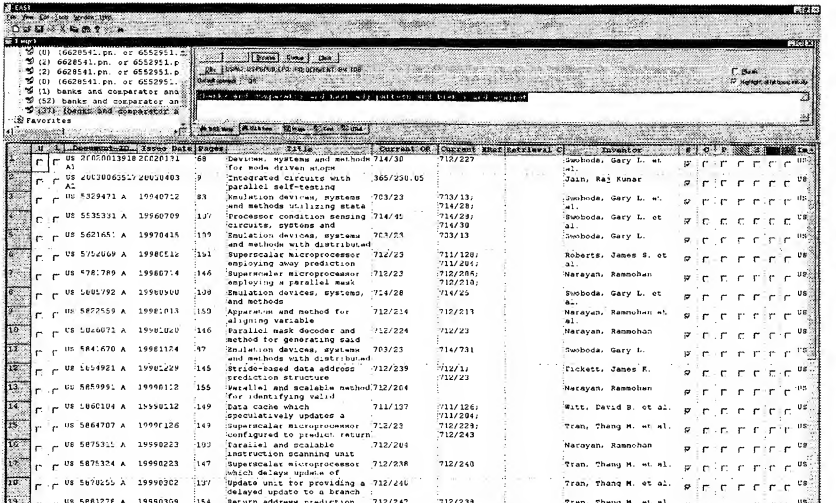
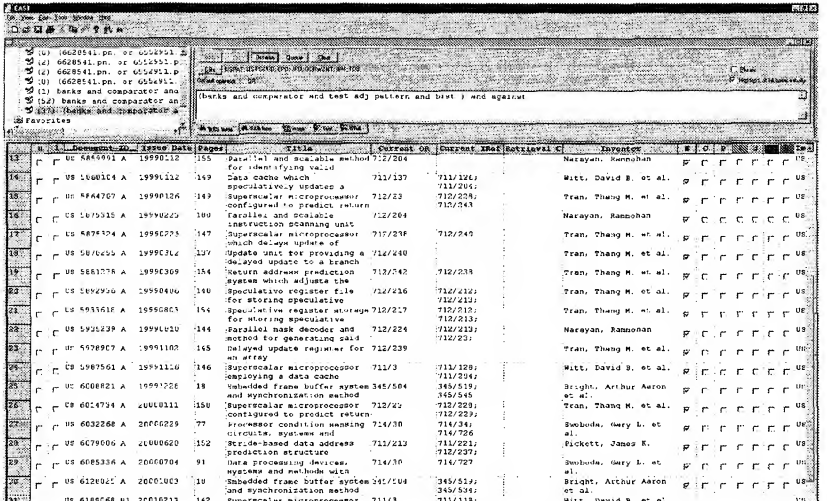


	M	L	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	F	O	P	S	JPN	EP
1			US 20030013918 A1	20020731	68	Devices, systems and methods for mode driven stops	714/30	712/227		Swoboda, Gary L. et al.						US
2			US 20030065917 A1	20030403	9	Integrated circuits with parallel self-testing	365/230.05			Jain, Raj Kumar						US
3			US 5329471 A	19940712	83	Emulation devices, systems and methods utilizing state	703/23	703/13; 714/28;		Swoboda, Gary L. et al.						US
4			US 5535331 A	19960709	117	Processor condition sensing circuits, systems and	714/45	714/28; 714/30		Swoboda, Gary L. et al.						US
5			US 5621885 A	19970415	159	Emulation devices, systems and methods with distributed	703/23	703/13		Swoboda, Gary L.						US
6			US 5752069 A	19980512	191	Superscalar microprocessor employing away prediction	712/23	711/128; 711/201;		Roberts, James S. et al.						US
7			US 5781789 A	19980714	146	Superscalar microprocessor employing a parallel mask	712/23	712/206; 712/210;		Narayan, Rammohan						US
8			US 5801792 A	19980900	109	Emulation devices, systems, and methods	714/28	714/25		Swoboda, Gary L. et a.						US
9			US 5872959 A	19981013	159	Apparatus and method for aligning variable	712/214	712/213		Narayan, Rammohan et al.						US
10			US 5926072 A	19981020	146	Parallel mask decoder and method for generating said	712/224	712/23		Narayan, Rammohan						US
11			US 5841674 A	19981124	47	Emulation devices, systems and methods with distributed	703/23	714/731		Swoboda, Gary L.						US
12			US 5854921 A	19981229	145	Stride-based data address prediction structure	712/239	712/1; 712/23		Tickett, James K.						US
13			US 5889981 A	19990112	155	Parallel and scalable method for identifying valid	712/204			Narayan, Rammohan						US
14			US 5860104 A	19990112	149	Data cache which speculatively updates a	711/137	711/126; 711/204;		Witt, David B. et al.						US
15			US 5864707 A	19990126	149	Superscalar microprocessor configured to predict return	712/23	712/223; 712/243		Tran, Thang M. et al.						US
16			US 5875324 A	19990223	193	Parallel and scalable instruction scanning unit	712/204			Narayan, Rammohan						US
17			US 5875324 A	19990223	147	Superscalar microprocessor which delays update of	712/238	712/240		Tran, Thang M. et al.						US
18			US 5974535 A	19991026	127	Update unit for providing a delayed update to a branch	712/240			Tran, Thang M. et al.						US
19			US 5881278 A	19990309	154	Return address prediction system which adjusts the	712/243	712/239		Tran, Thang M. et al.						US
20			US 5996066 A	19991406	110	Speculative register file for storing speculative	712/216	712/212; 712/213;		Tran, Thang M. et al.						US
21			US 5553616 A	19980803	154	Speculative register storage	712/217	712/212;		Tran, Thang M. et al.						US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